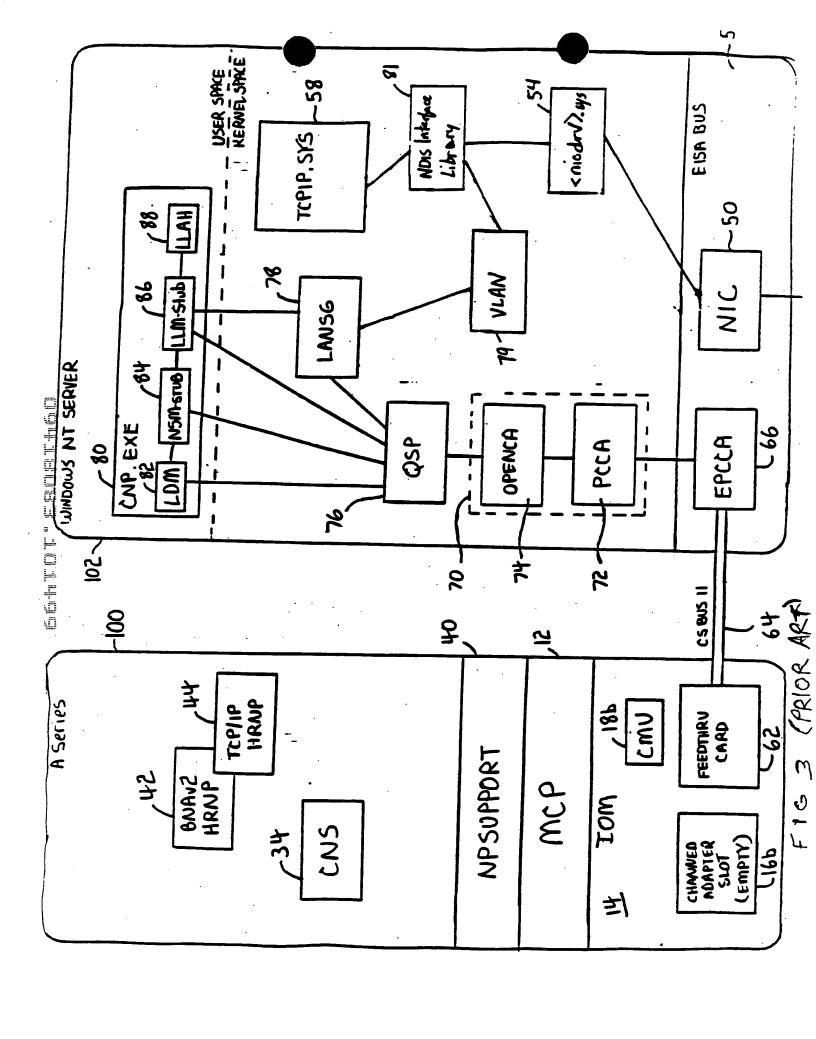
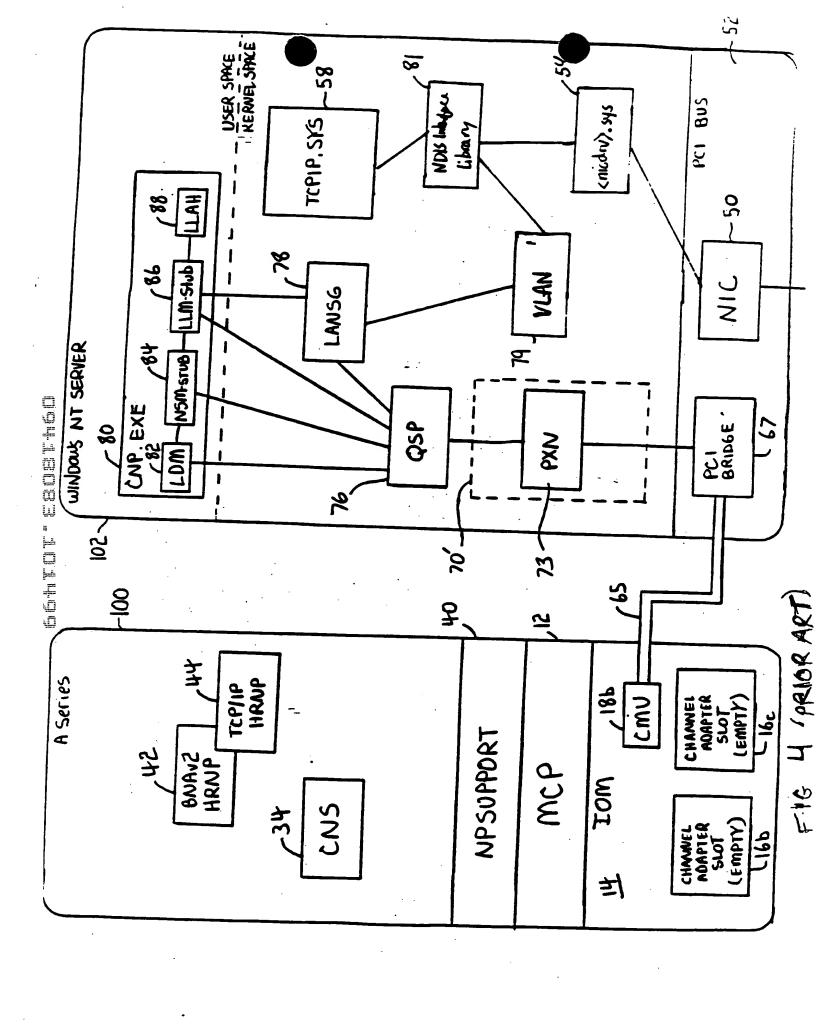


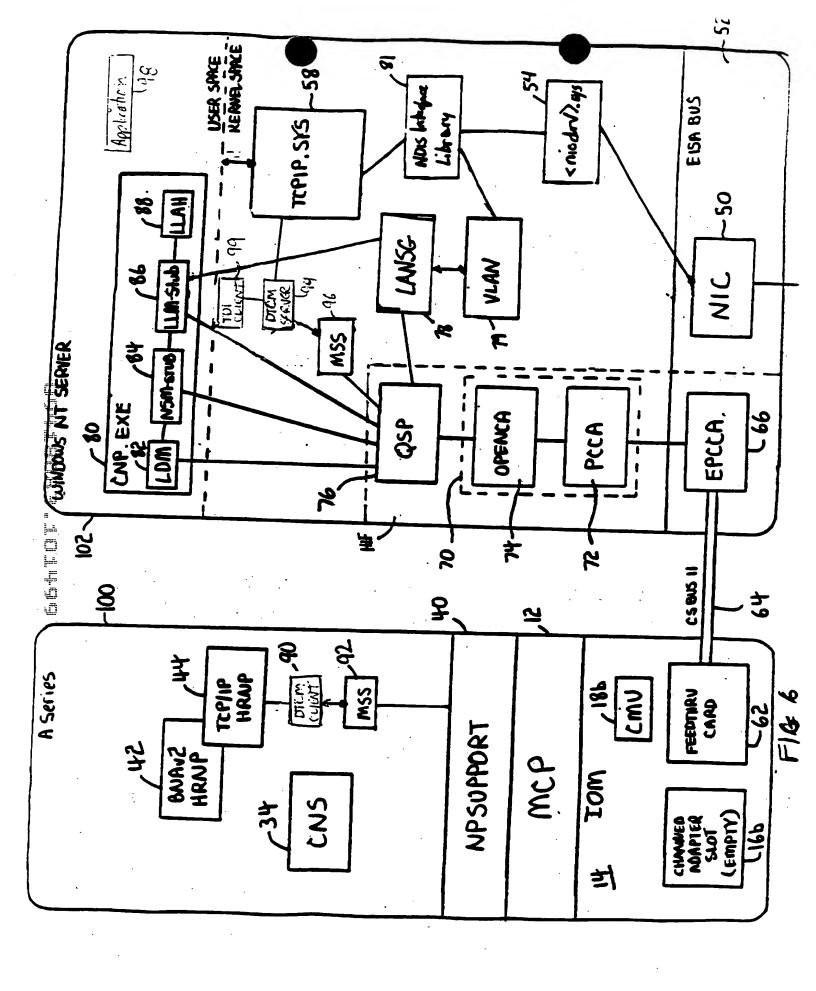
FIG. & (Prior Art)

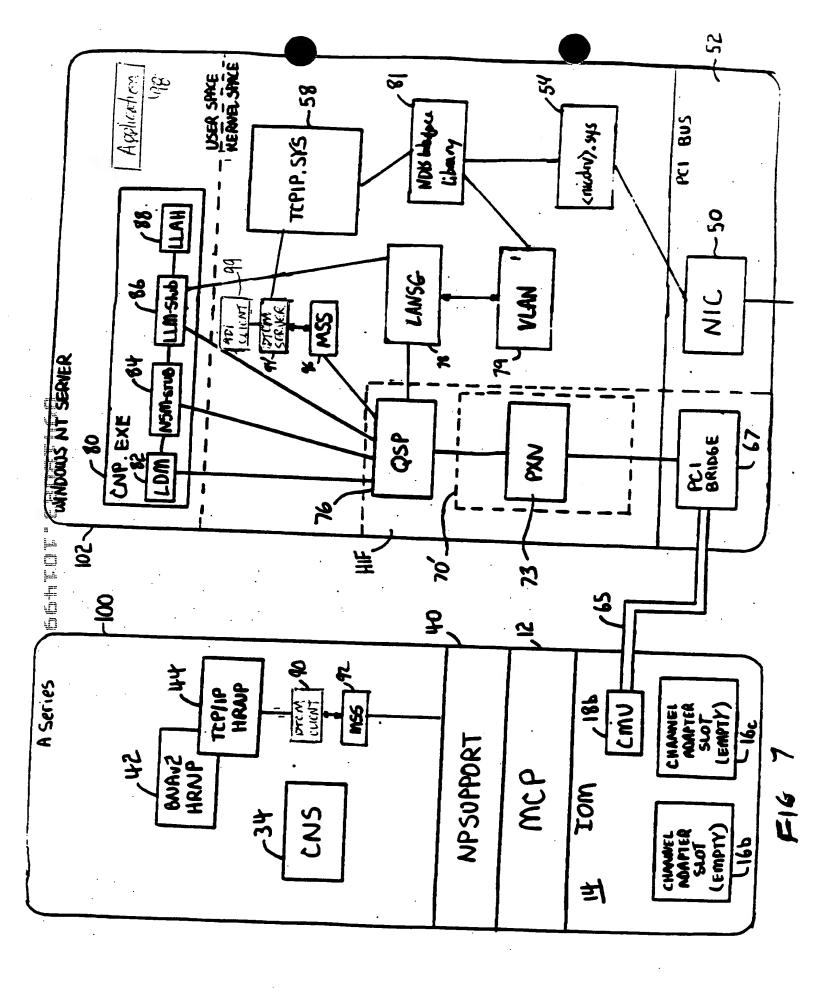
ſ

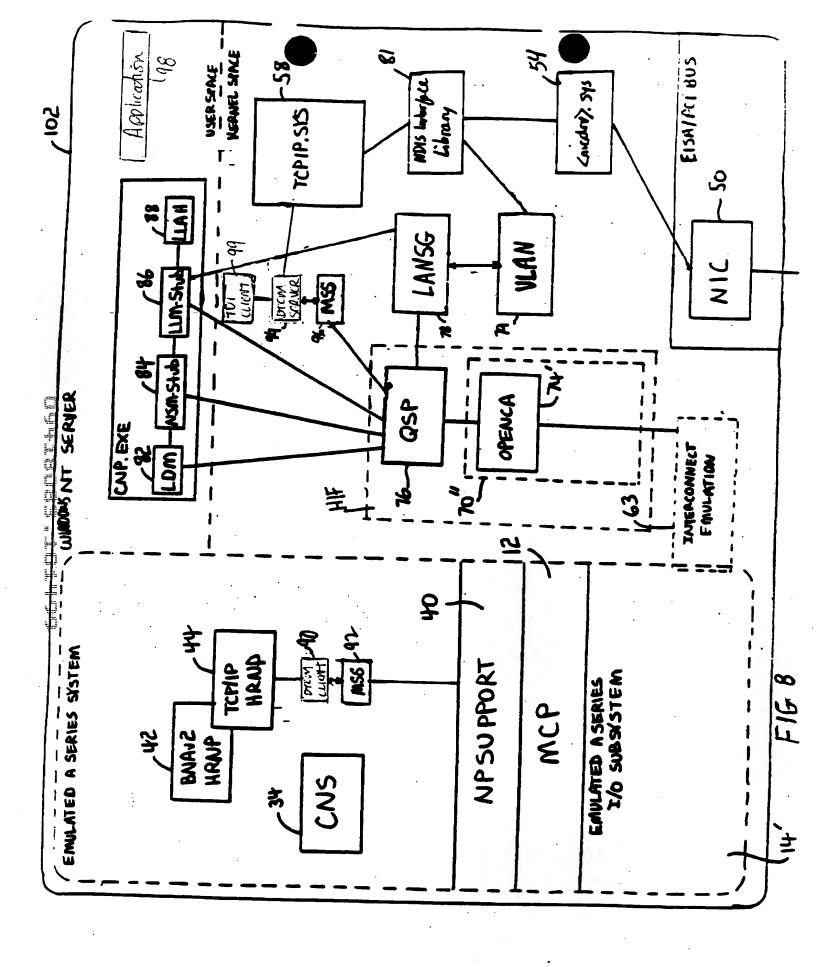
SL











Client calls QSP, passing a request which points to non-contiguous message segments

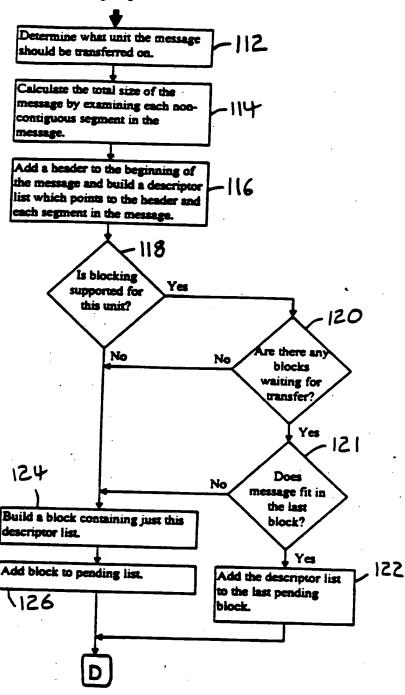
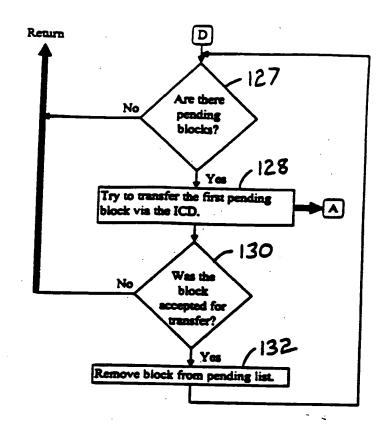
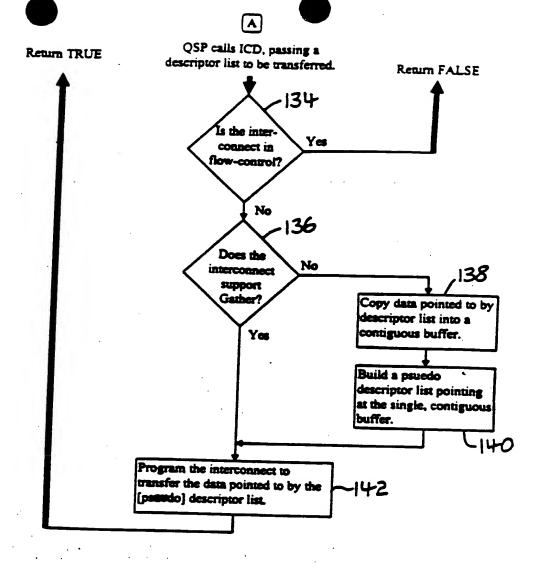


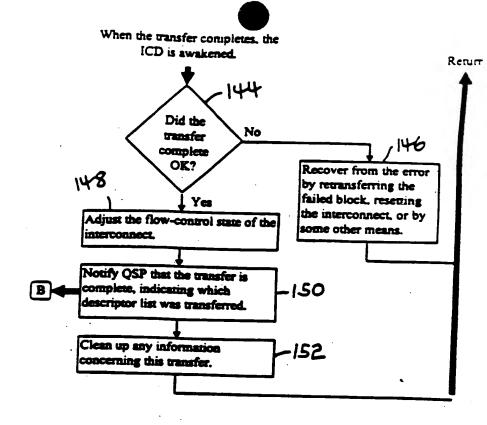
FIG 9A



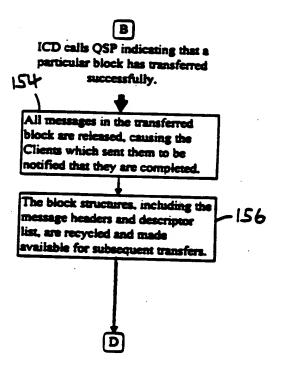
F16. 9B



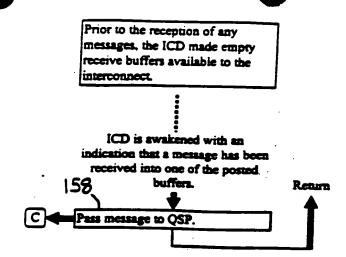
F16.90



OSP:



F16.9D



OSP:

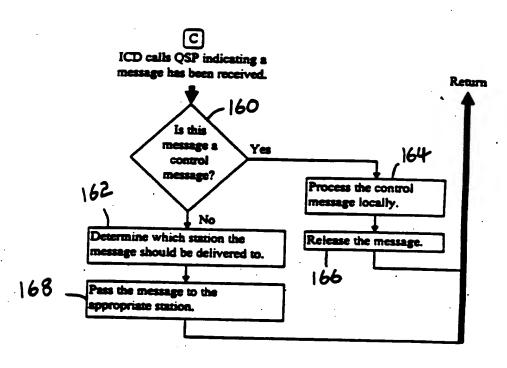
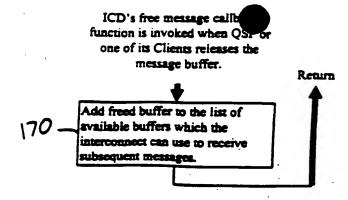


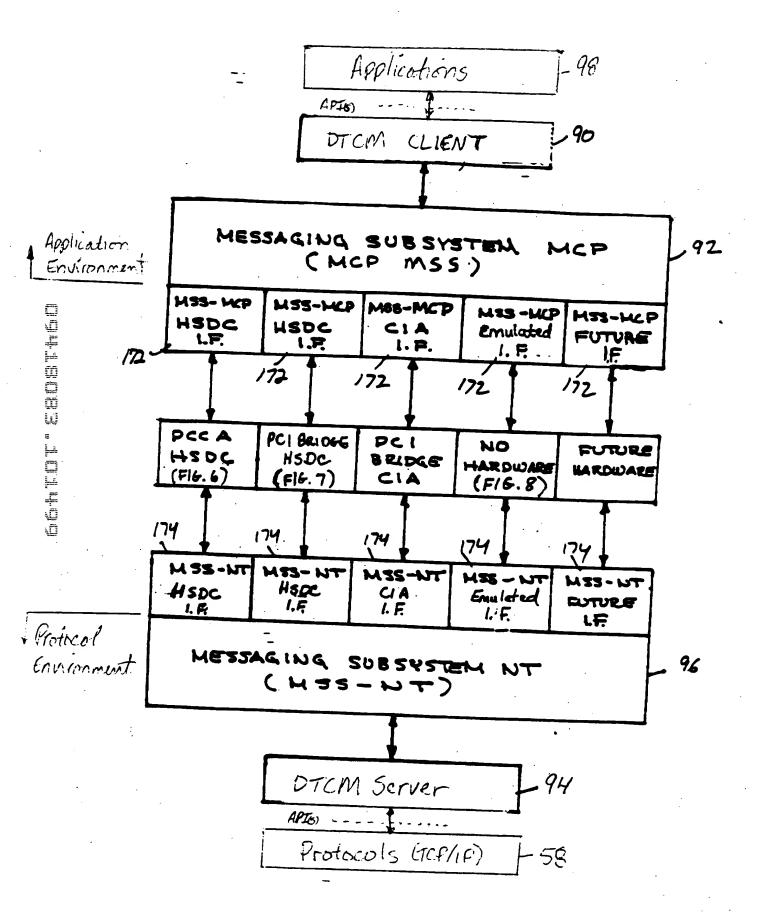
FIG. 9E



F16.9F

. İ

FIGURE 10



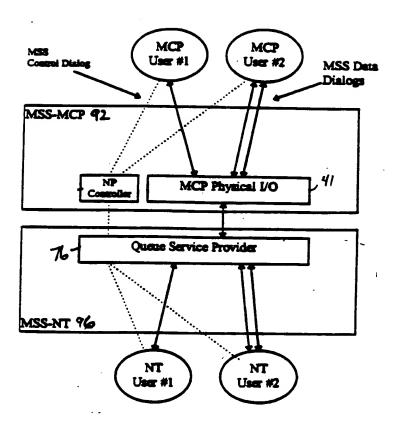


FIGURE 11

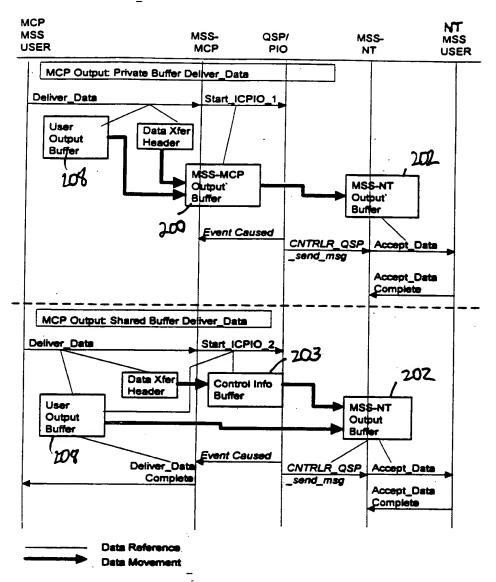


Figure /2 QSP-Based Output Data Transfer Flow Diagram

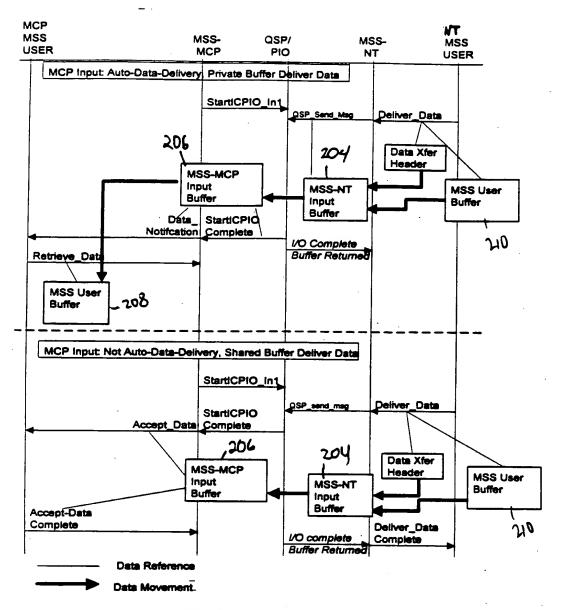


Figure 13 · QSP-Based Input Data Transfer Flow Diagram

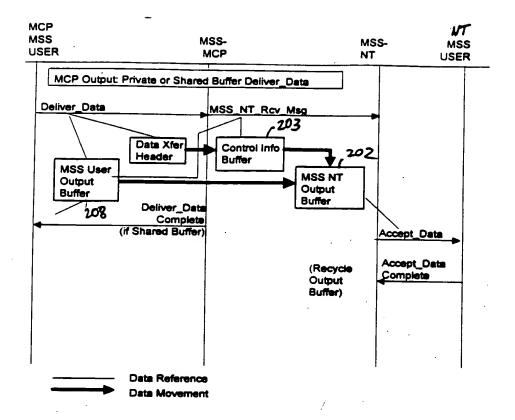


Figure |4 Emulation-Based Output Data Transfer Flow Diagram

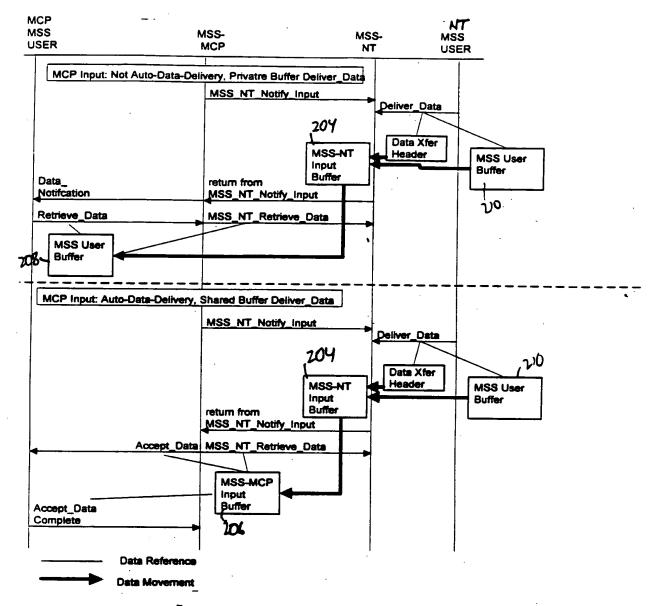


Figure 15 Emulation-Based Input Data Transfer Flow Diagram

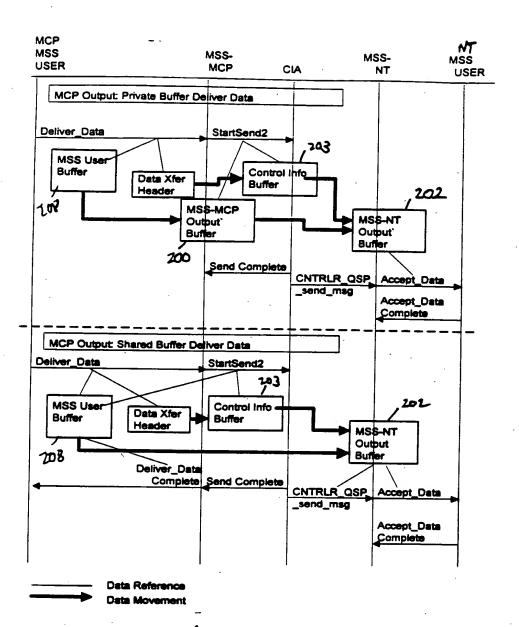


Figure 16 CIA-Based Output Data Transfer Flow Diagram

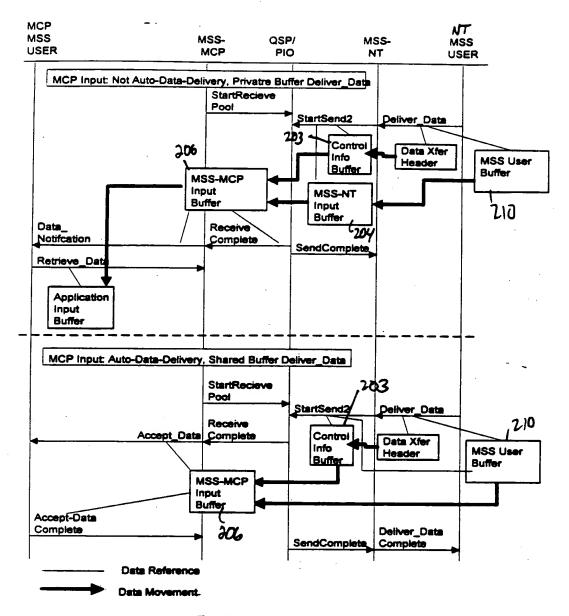


Figure 17 CIA-Based Input Data Transfer Flow Diagram

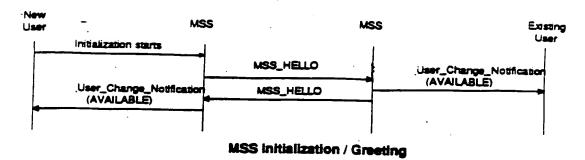


FIGURE 18

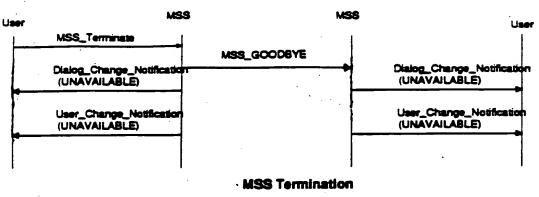


FIGURE 19

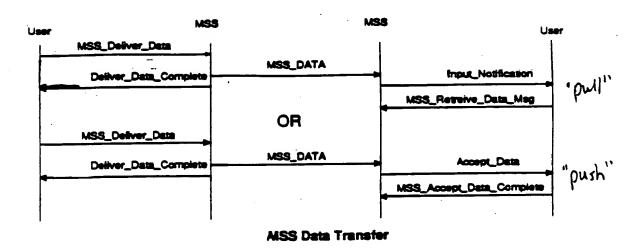
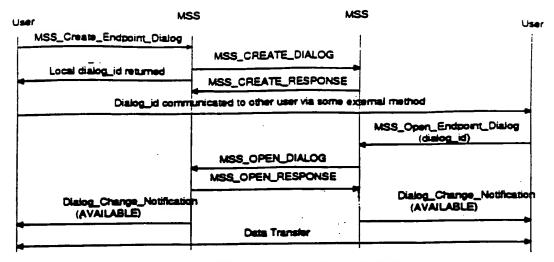
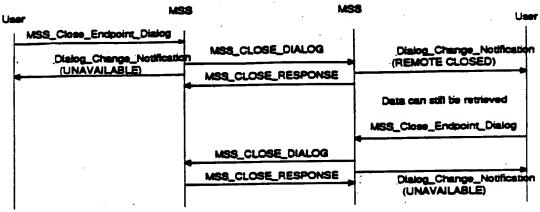


FIGURE 20



MSS Endpoint Dialog Creation

FIGURE 21



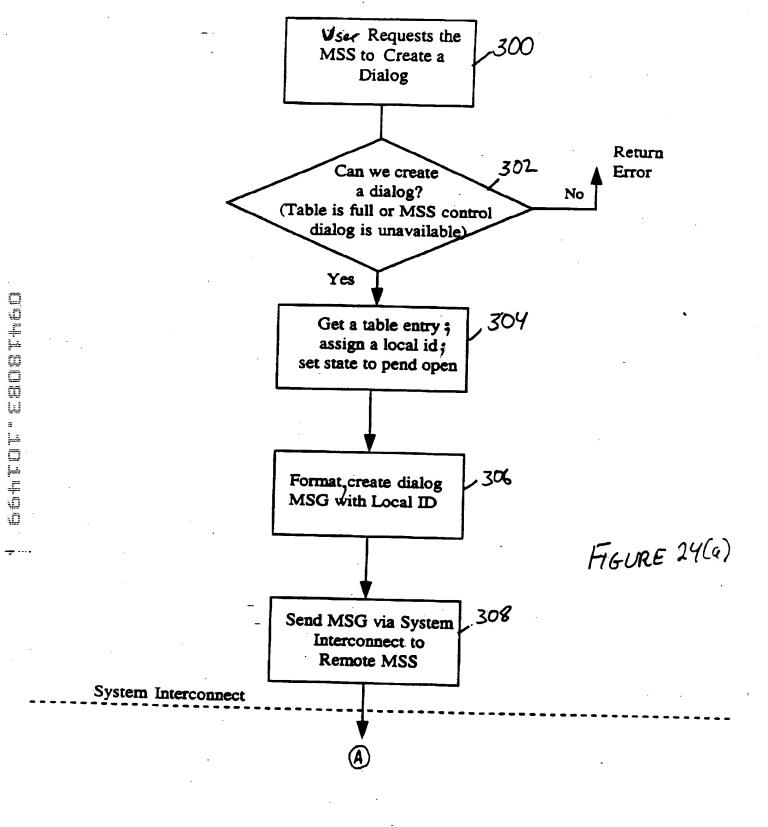
MSS Endpoint Dialog Termination (Normal Close)

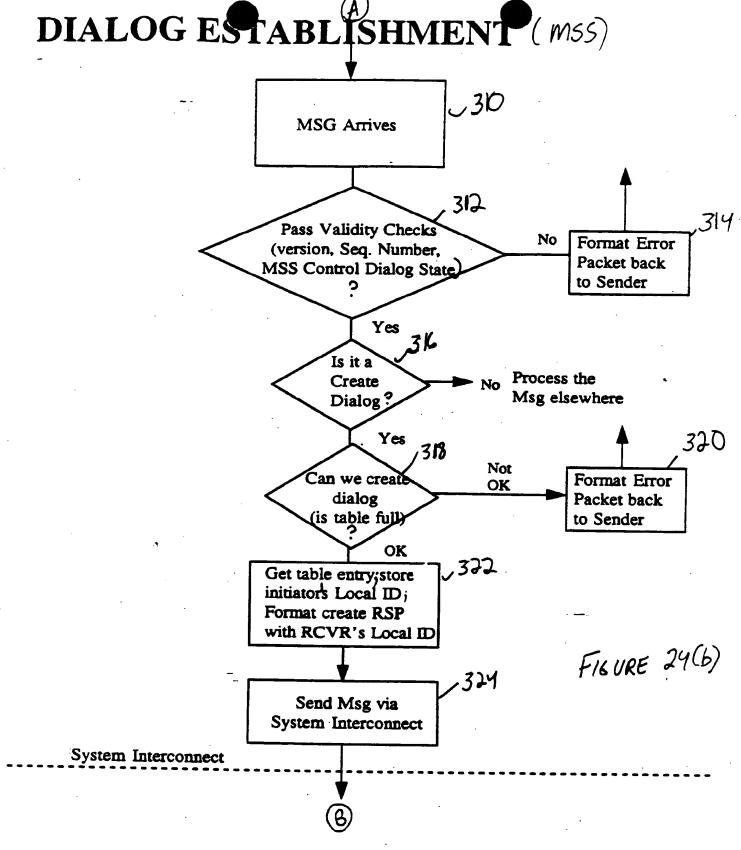
FIGURE 22



MSS Endpoint Dialog Termination (Destroy)

FIGURE 23





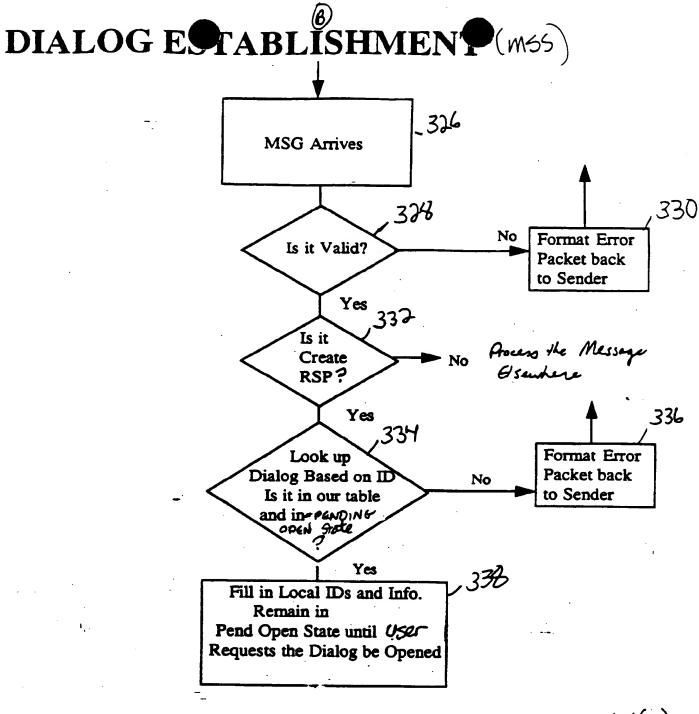
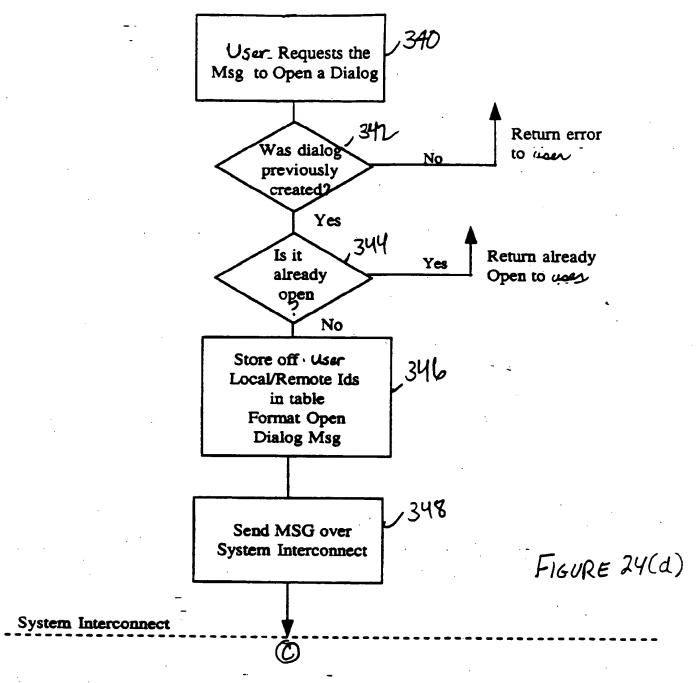
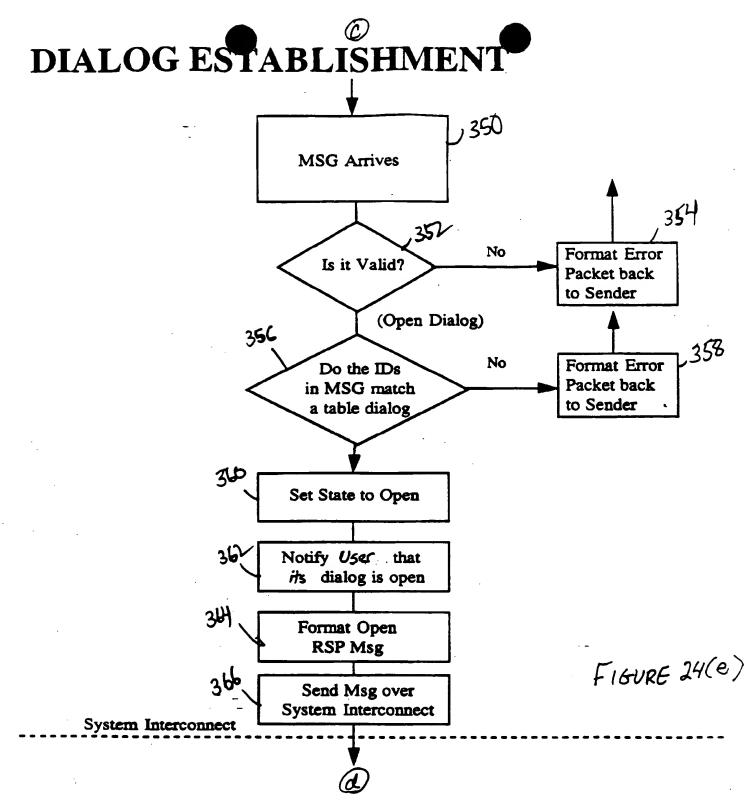


FIGURE 24(c)



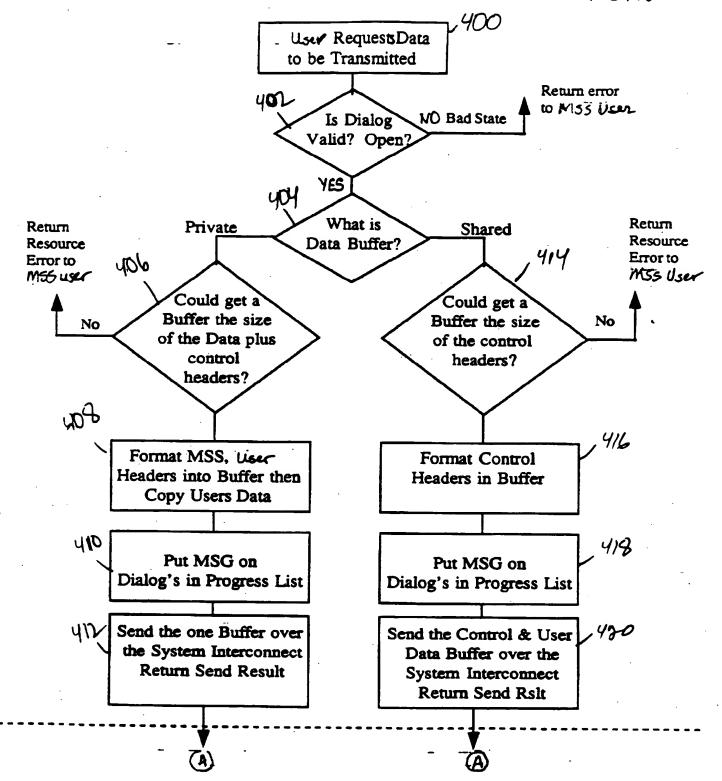


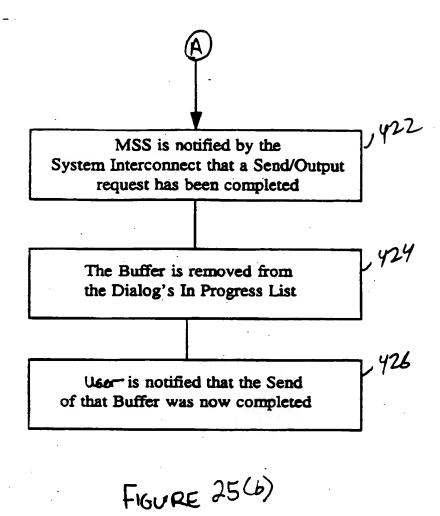
DIALOG ESTABLISHMENT 368 **MSG** Arrives 372 ,370 No Format Error Is it Valid? Packet back to Sender Yes (Open Dialog) 374 376 No Format Error Do we have Packet back such a dialog? to Sender 378 Mark Dialog "Opened" ,380 Notify User that its dialog is open

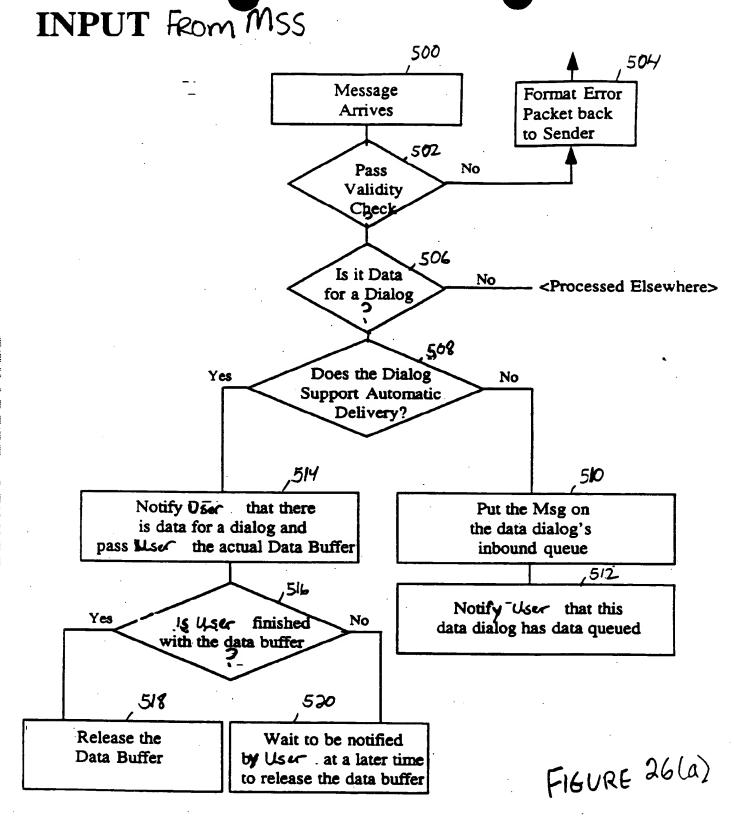
FIGURE 24(f)

Output Over 9955

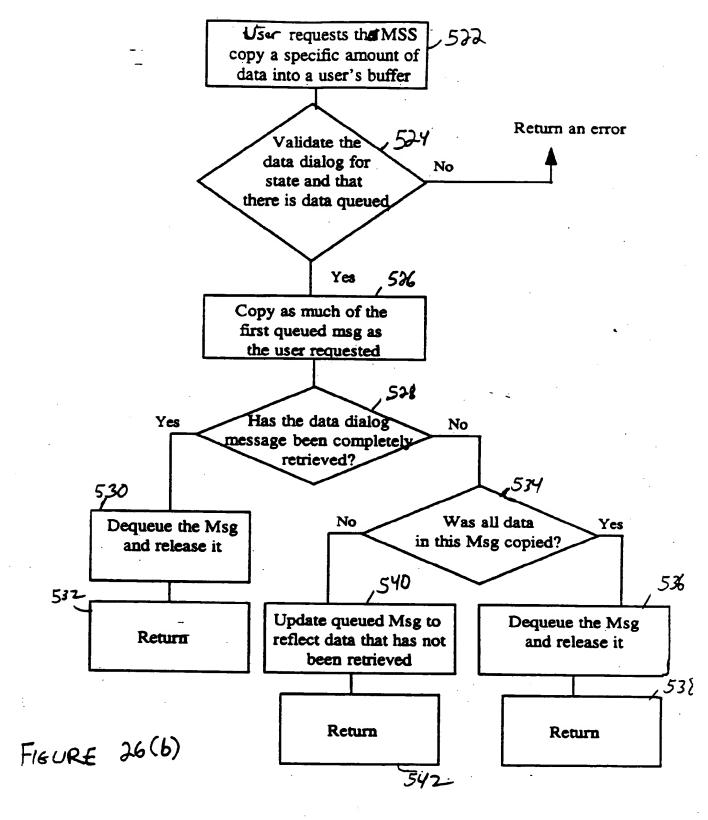
FIGURE 25(a)







INPUT FROM MSS



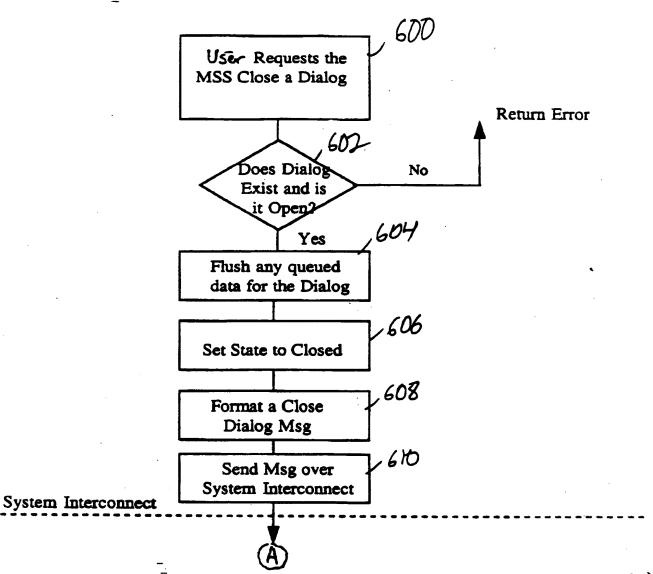
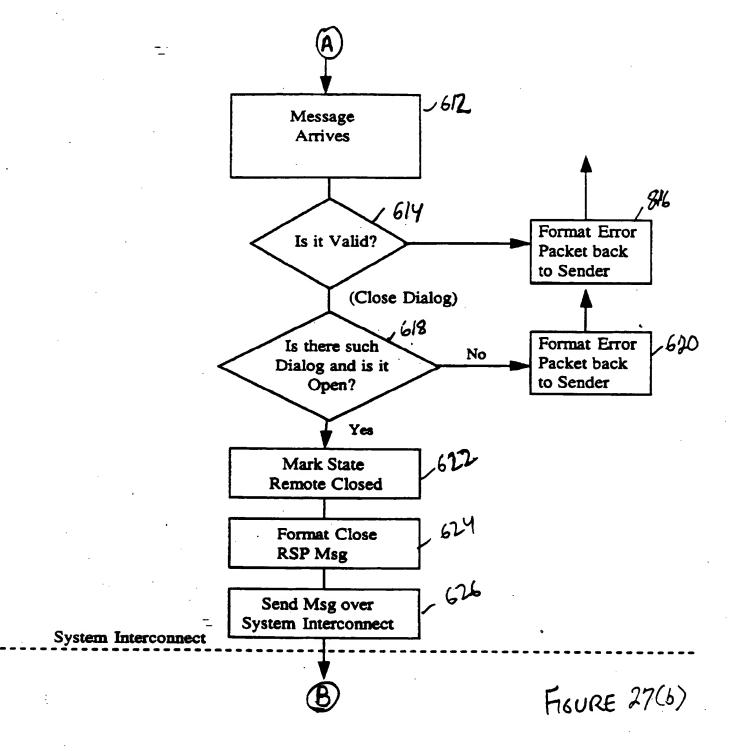
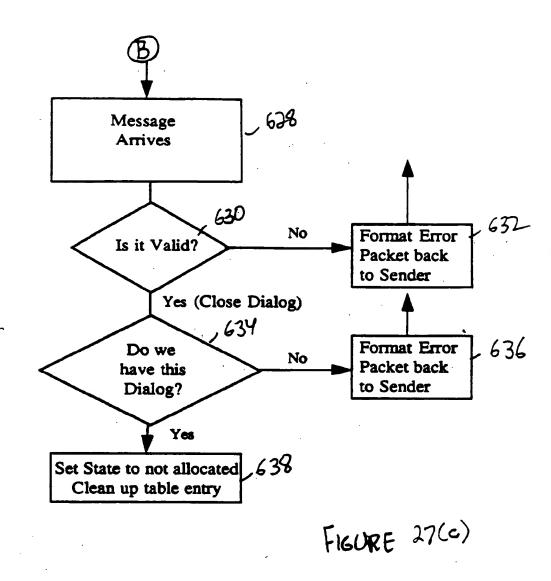


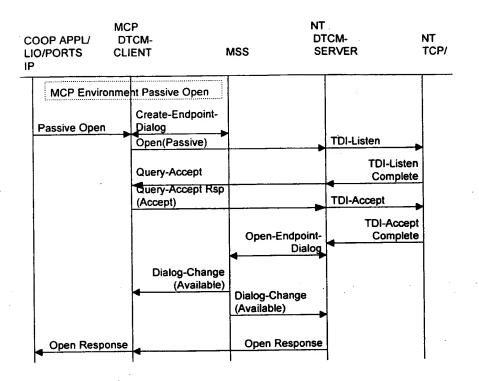
FIGURE 27(a)

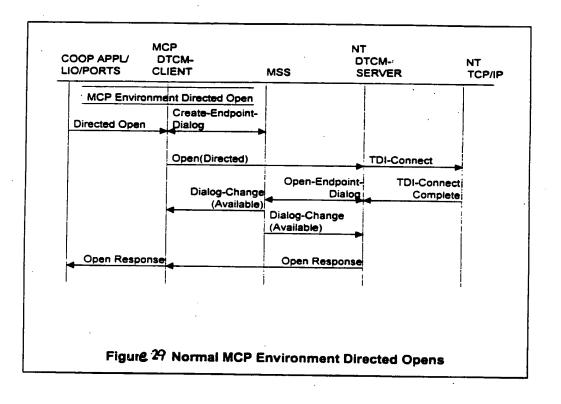


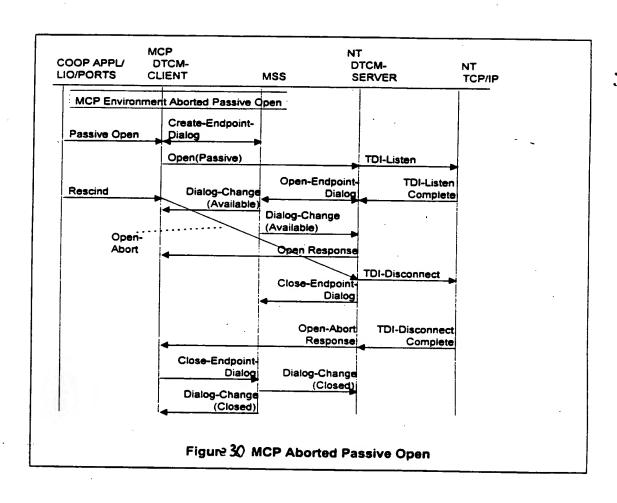
DIALOG TERMINATION (SS)

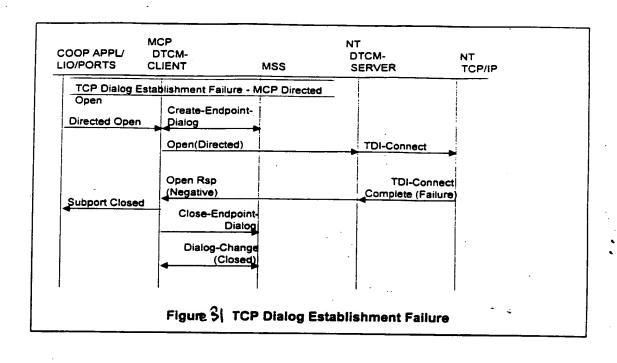


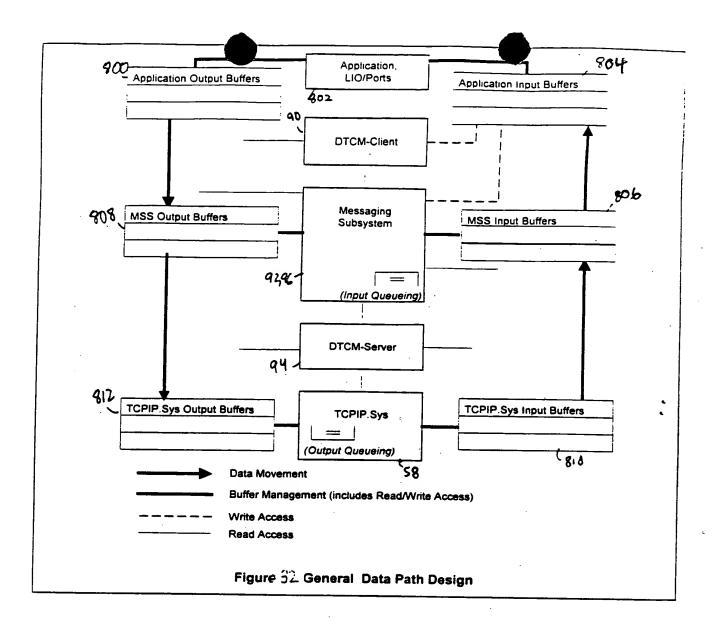
"Figure 28 - Normal MCP Passive Open"

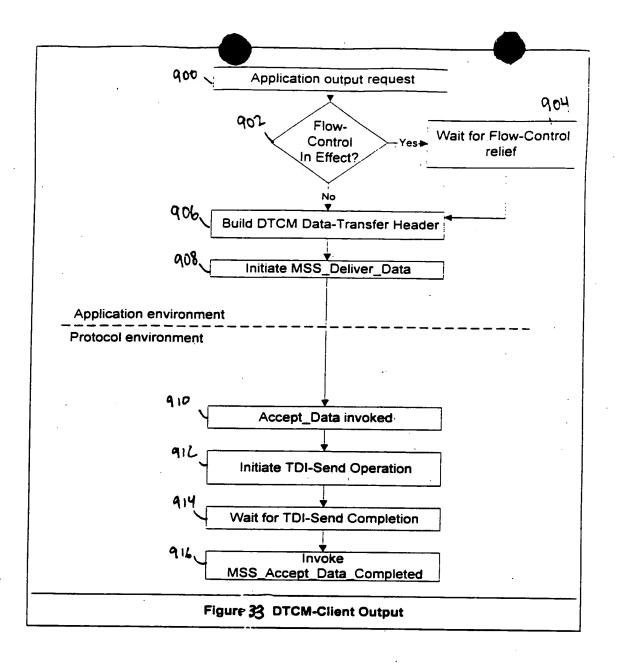












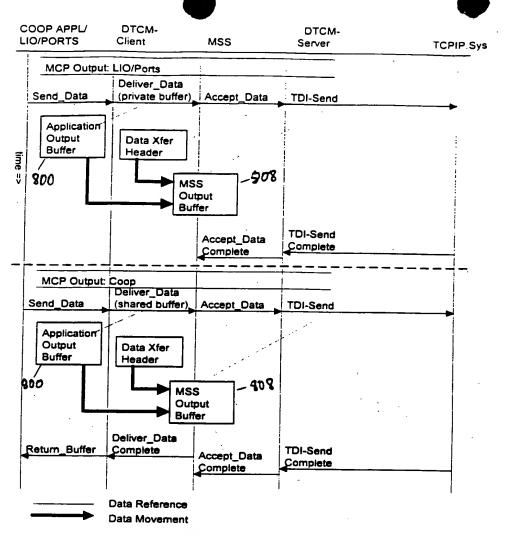


Figure 34 General Output Data Transfer Flow Diagram

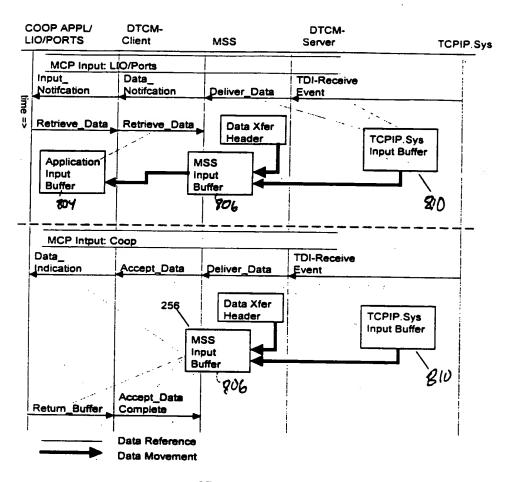


Figure 35 General Input Data Transfer Flow Diagram

